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<u>Filing</u> <u>Number</u>	Country Code	Filing Date	Page 1 of 7 Title / Inventors (US Patent)
699;600	ARIUS T	1.4/05/1.991	Methodiformanufacturing amactive matrix displayiscreen:
264,199	US'	22/06/1994	Active matrix display screen with storage capacitors (formed of conductive blocks, semiconductive material) nonconductive material; and capacitive lines (Morin) (1).  Le Confeller (1) (1)
+792\$262 + 91403055.6 + 90.14306 + 91403055.6 + 91403055.6	DE PPR FR/EP	14/11/1991	Methodsfor etching integrated circuit layers to a fixed depth
7,64,211	CIUS SESSES		Protective circuit for accontrol circuit, in particular of liquid vie 4. Ciystal display screen / Senn, Eclah, Martelt
764,196	i; i;US ≟i		Sample and bold-circuit for a liquid crystal display screen/ Sean; Lelali, Martel
## 896,248 192420047/0 <sub>11</sub> 291,01984	e CER		Collector of a bipolar transistor compatible with MOS 213 12 24 technology/Nonailhat Bois 1
832,078 + 12 + 12 + 12 + 12 + 12 + 12 + 12 + 1		06/02/(1992 <u>)</u> 07/02/(1994)	Procession buried localized oxidation of a silicon substrate and corresponding integrated circuit / Straboni, Barla;  Wuillermoz
- 16,866,342= 44	∂ii-US⊟i	10/04/1992	Process for the production of thin film transistors//Chouan 21
9240190944 9240190944 9240190944 5 191108677	DE GB		Methodiof manufacturing a vertical fieldleffect transistor // Ghantre, Bois, Novallhau
9.110.13 1 17.9240.1987.10 204.2365 19240.1987.05	US GB TR	09/07/1992 09/07/1992 09/07/1992 09/07/1992	Active matrix, high definition, liquid/crystal display/structure/// Morin, Chouan, Vinouze

<u>Filing</u> <u>Number</u>	Country Code	Filing Date	Page 2 of 7 Title / Inventors (US Patent)
231,024 a.,	# USC S	20/04/1994	Method of clock signal recovery and of synchronization for the reception of information elements transmitted by an ATM
			network and device for the implementation of the method
94400853.1	DEA	19/04/1994	Rainard
93.04775	FR	22/04/1993	
94400853:1	GB <sub></sub>	19/04/1994	。 対対では、特別を持ち、中に名書を対する。 対対では、特別を持ち、中に名書を対する。 対対できた。 対はできた。 はできたた。 はできた。 はでをとできた。 はできたた。 はできたた。 はでをとでを、 はでをできた。 はできたを、 はできたを、 はで
228,479	er.Us.	15/04/1994	Signal processing device using several different filterings; he especially for audio frequency coding of voice signals.
			Balestro Senn
94400780:6 93-04532	DE FR	11/04/1994 16/04/1993	
9440078016	GB⊭	11/04/1994	
279,546	. USHE	25/07/1994	Optical amplifier havinga doped fluoride glass optical fibre
p i			and process for producing this amplitier // Ronarc H. Quibert, **** I tounied**
94401706.0	GB)	25/07/1994	
변93i 09년65년 - 9440년706:0	FR	26/07/1993 25/07/1994	
94401706.0	, <b>in</b>	25/07/1994	
299,663	. USE		Process for the transposition of amortical modulation of one ass
			wavelength to another adjustable wavelength / Dupont
94401984:3	GB -	-07/09/1994	
94401984.3 93/10737	FR.	07/09/1994 09/09/1993	
385,953	- EUS	09/02/1995	Degreasing device particularly for optical fibers /Crespel,
			Gailleaux Caudrelien 4 - 13 - 13 - 13 - 13 - 13 - 13 - 13 -
94 01673 195400232.5	FR- DE	-1-1/02/1994 - 03/02/1995	
95400232.5 95400232.5	GB) IT	03/02/1995 03/02/1995	
	Table 1		
377,472	w USLA	24/01/1995	Optical amplifier with a döped fluoride glass of optical fibre and process for producing said amplifier // Semenkoff
			Ronarch, Guibert
492,591	US	20/06/1995	Electronic memory addressing device especially for ac-
95401444:5	. DE	20/06/1995	niemory(organized intoibanks / Quillevere, Duthil
94 07579	FR.	21/06/1994	

<u>Filing</u> Number	Country Code	Filing Date	Page 3 of 7 Title / Inventors
			(US Patent) Electrically conductive polymer compositions, by oduction 35 114
564,120 · · · · · · · · · · · · · · · · · · ·	US	-30/03/1995	process and coated substrates / Clarisse Delabouglise (
regularity and the second seco			Ciprelli, 1977
\$2915230:7 \$2915230 7	.is GB si DE,	30/03/1995= 30/03/1995=	
94 03812	FR	31/03/1994	
La Paroa signa	New York	*** to/00/1005	
至1527月375~ 1216年2月2日	UST.	12/09/1995	Sequential access asynchronous memory device and [1, 4] [2] Corresponding process for storage and acading / Majos
			Weili 2
\$\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	DE FR	04/10/1995 a 12/10/1994	
95402243 3	GB <sub>i</sub>	404/10/1995	
C=569 8 19/d	. US	08/12/1995	Electronic component capable, in particular of performing
			a division of two numbers to the base 4 / Dufal Robert 1, 4 + 44
94,15504 95402809.8	FR: DE'	22/12/1994 13/12/1995	
35402809.8	GB	13/12/1995	
**************************************	(水)(本)( <b>)</b> (在服器	94/04/Jane	
26391085	US	29(04/1790 1 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	Device for programmable delay of an analog signal and it is a second corresponding aboustic antenna #Lie Tourneur, Balestro!
96400851.0	FR/EP	22/04/1996	
4.9640085010 9640085110	DE GB:	22/04/1996 22/04/1996	
95/0491/8	FR	25/04/1995	
08/603,620	US	= 21/02/1 <b>9</b> 96	Minimizing program code storage for sperforming regular and 4
			repetitive operations in a programmable processor / Dufal?
96400333.9	DE	÷19/02/1996	Private de la companya de la company
95,02.1316	PR	-23/02/1995	
9640033319	GB	19/02/1996	
15 7 67.4; 67.5 <del>1</del>	÷่⊣∵็บร≟์	. 02/07/1996	-Apparatus and a method-for identifying and splicing
			inpliticore fibers // Le Noane, Perrin Le Marer   144   144   144
9614093.4 95.08042	GB FR	∷04/07/1996 - 04/07/1995	
to and the programme of the state of the sta			
309/462,7:16 <sub>5</sub>	TUS I	The said of the said of the said	Methodlof minimising the corner effect by densifying the insulative layer //Schiavonne Gaillard
*2000-502,539		/08/07/1998	
9 <b>8</b> 93648017; 19 <b>8</b> 9364 <b>80</b> .7.		≟-08/07/1998.∃ ≟-08/07/1998⊒	
97.08642	'FR'	08/07/1997	

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98401432.4 97.07988 1178,749	EP'	.23/06/1998 12/06/1998 25/06/1997 25/06/1998	Process for obtaining a transistor having a silicon germanium  gaic // Sagnes : 1111   1211
98401410:00 97107892	EP.	-15/06/1998 	High-deusity and high-capacity distribution frame for optically fibers /Lamerce/Blanchard, Gueguen
09/384,362 1 1994021/2012 58-10768	FR	25/08/1999h 27/08/1998	
09/289\667 	USF LEP JP FR	08/04/1999	Electrooptic method of signal processing, device for mplementing the latter, and use //Ferriei
09/986;487 7.11 4.99.02823 89104000 9.00909440:0	THE PERSON AND ADDRESS OF THE PARTY	.07/03/2000 .08/03/1999 .07/03/2000 .07/03/2000 .07/03/2000	Process for testing integrated circuits with accesstoches memory points of the circuit/ Barthel
7.00929626:02 100929626:02	FR	18/05/2000- 120/05/1999 18/05/2000 18/05/2000	Complex number multiplier // Montalvo, Arndt
99 03/69		-23/02/2000 -15 35 (15/03/ -23/03/1999) -24/02/2000	Dual mode radio fréquency, reception device and recorresponding multimedia, receiver // Andre, Seinn
99.04052 091;660 00400703;5	ER P		Process for fabricating applanar, heterostructure:/ Hemandez_Campidelli, Rivoire   Bensahel

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<u>Filing</u> Number	Country Code	Filing Date	Page 5 of 7 Title / Inventors	
09/553,91/6	US Z	20/04/2000	(US Patent) Integrated circuit device comprising an undustor with high quality coefficient / Merckel, Rons, Senn, Rournier	
99.04986 00401086:4	i iR:	20/04/1999 19/04/2000	quanty coefficient, via coefficient, from the coefficient of the coeff	
118,658.	Ţ.	19/04/2000		
10/019/170	F US	23/06/2000 29/06/1999	Method-for compensating non-linearity of a sigma-delta analog-to-digital converter / Morche]	
09/518,944	. ¡ US .	<u>06/03/2000</u>	Radiofrequency transmitter with a high degree of	
.00460019.3	I- ED	02/03/2000	integration and possibly with self-calibrating image deteuon/	
99 03768	FR:	23/03/1999		
09/559,524	US	28/04/2000	Frequency control system-that stabilizes an output through both a counter and voltage-controlled oscillator via sampling.	t)
00401100.3	EP	19/04/2000	a generated clock into four states / Majos	
99 05627 129,903	FR I	30/04/1999 28/04/2000		
09/505;448	US	16/02/2000	Process for the anisotropic etching of an organic dielectric.  polymer material by a plasma gas and application in	
99 01925	∓ FR. ∓	17/02/1999	niicroelectronics // Joubert, Fuard	
030,132 00400363.8	JP EP	-08/02/2000 -09/02/2000		
10/018,179	US.	05/06/2000	Semiconductor device with compensated threshold voltage:	
99(07394	EP FR	05/06/2000 11/06/1999		
	US	26/05/2000 26/05/2000	Band-pass filter with carrier frequency reduction/Morche	
= 00936952.11 = 00936952.11 = 99/067/10	IT PR	26/05/2000 26/05/2000 27/05/1999		
10/018,680	US	= 08/06/2000	Method for making a silicon substrate comprising, a burieds :	
99'07496	FR	14/06/1999	thin silicon oxide film / Jurczak, Skotnicki	
00940457,5	LEP:	= 08/06/2000 ·	是一个一个一个一个一个一个一个一个一个一个一个一个一个一个一个一个一个一个一个	

<u>Filing</u> Number	Country Code	Filing Date	Page 6 of 7 Title / Inventors
09/620;896	US A		(US Patent)  Methodioficorrecting topographical effects on a unicro-
111111111111111111111111111111111111111			electronic substrate /Schiltz, Paol; Schiavone, Prola
° 1 219,525	jp.	19/07/2000	
2000-0042258	· KŘ	22/07/2000 22/07/1999	
99.09521 00402053\3\	FR GB	, 22/07/1999; -19/07/2000	
00402053:3i		19/07/2000	
Electronic Control of the Control of	Sec. 27.10/2	108/06/2001	Low-noise spectroscopic ellipsometer/Petricu
10/168,041 00.07425		08/06/2000 09/06/2000	Edwinings specific conjugation of the conjugate specific conjugate spe
\$_01945381.0 <sup>m</sup>	EP	08/06/2001	
2002-502,401	TO JP.	7 08/06/2001	。 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1
09/912/057	· US	325/07/2001	Resint a double resim layer for extreme ultraviolet (BUV)
			photolithography, and an extreme ultraviolet light (EUV)
01401972.3	ER	23/07/2001	photolithography process / Sohiltz
00 09759	FR	26/07/2000	
10/380.756	- US	18/09/2001	Device for punctual measurement of a radiofrequency magnetic field with constant amplitude and frequency/
			Bouvier Geoffroy
00 11923	**** *** ** *** *** *** *** *** *** **	19/09/2000	And the second s
01969920.6 01969920.6	DE FRÆP	18/09/2001 18/09/2001	
528842/2002		18/09/2001	
01969920.6	÷GB <sup>™</sup>	1i8/09/200iju	
10/332,532	· US	10/07/2001	Saturable absorbent structure, imparticular for regenerating 💨 😘
			optical component and method for making same / Marceaux;
400 09030		11707/2000	Loualione, Le Corre, Dehaese
01954065.7	FR EP**	10/07/2000	
2002-509824	JP	10/07/2001	
2,416,127	INSTICATION	4 10/07/2001 4 10/07/2001	[1][[1][[1][[1][[1][[1][[1][[1][[1][[1]
01 03829	FR		Method and device for transmitting a video sequence.
			comprising a face, especially in a mobile videophone
02716894.7	THEP I	14/03/2002	system//Roux Petit

<u>Filing</u>	Country	Filing	Page 7 of 7 Title / Inventors (US Patent)
<u>Number</u>	Code	Date	
10725,973 1 02 03477 01 07032 102747487.3 20034500328	US FR FR FI	24/05/2002 22/05/2001 22/05/2001 24/05/2002 24/05/2002	Large expresity entemptic distributor, particularly for optic fibres; device and marked for automatic connection () disconnection or the bibliding fibres (Moralles, Le Turon).  Boughat

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Issy les Moulineaux on December 3<sup>rd</sup>, 2004

**ASSIGNOR** 

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Name: François JAMET

Title: Directeur de la Propriété Industrielle et de la Valorisation

(Signature MUST be notarized)

la signature de M. Manços apposée Ci. Ontre Paris, le 3 decembre

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